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	Filing Date		2006-07-25		
	First Named Inventor	Andre	Andreas Bode		
	Art Unit		1621		
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	2	SAYTINER, et al., "A Level-Crossing Sampling Scheme for A/D Conversion", IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing, New York, Vol. 43, April 1, 1996, Pages 335 – 339, page 335, left column, line 12 – line 13, XP 000592026								
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